

ORDER INFORMATION

5 R T D D R 5 0 N 0 N - B 8 S D P X L - 447 - R 1

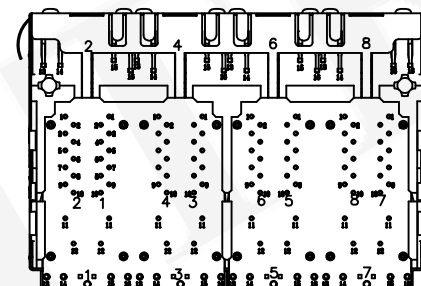
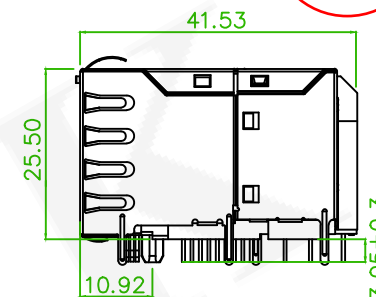
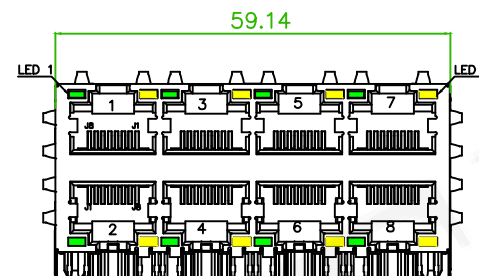


AMTEK 糧德

(2022.05.27)

Released DCC

The schematic diagram illustrates the PCB side of the PHY. It features a central vertical bus with various components connected to it. On the left, labeled 'PCB SIDE TO PHY', are inputs: VCC 9, TD1+ 2, TD1- 1, TD2+ 4, TD2- 3, TD3+ 6, TD3- 5, TD4+ 8, TD4- 7, GND 10, VC1 11, and VC2 12. On the right, labeled 'CABLE SIDE', are outputs: J1 TX1+, J2 TX1-, J3 TX2+, J6 TX2-, J4 TX3+, J5 TX3-, J7 TX4+, and J8 TX4-. The circuit includes four transformers (1CT:1) connected to the bus. A 2*0.1uF capacitor is connected to the bus between TD4- and GND. A 0.1uF 100VDC capacitor is connected to the bus between VC1 and VC2. Four 22nF capacitors (4*22nF) are connected to the bus between VC2 and the cable side. Four 75Ω resistors (4*75Ω) are connected to the bus between the cable side and the LEDs. A 1000pF/2.5KV capacitor is connected to the bus between the LEDs and the cable side. The LEDs are labeled LED 1 (13, 14) and LED 2 (15, 16).

[illegible]

LED Color Options Table 1		
Left(LED1)	Right(LED2)	No.
Yellow	Green	1
Yellow	Yellow	2
Green	Yellow	3
Green	Green	4

LED Specification

Standard LED Color	LED Wavelength	Foward(A)	Foward(V)
Green	568nm	20mA	2.2 -2.4V
Yellow	585nm	20mA	2.1 -2.5 V



糧德精密有限公司
Amtek Technology Co., Ltd

TEL: 886-2-2260-6878
FAX: 886-2-2260-8030

品名 TITLE	MODULAR JACK SHIELDED SIDE ENTRY,1G TRANSFORMER				頁數 Page	1/1
品號 PART NO.	5RTDDR50N0N-B8SDPXL-447-R1			繪圖 DRAWN	Alice 2022/05/27	
圖號 DRAWING NO.	GN169-AB340	版本 REV.	A	核對 CHECKED	Josh 2022/05/27	
客戶品號 CUSTOMER P/N		比例 SCALE	None	承認 APPROVED	Josh 2022/05/27	
公差 TOLERANCE	.X ±0.30 .XX ±0.25	單位 UNIT	mm	THIRD ANGLE PROJECTION		

REV.	ECN NO. OR DESCRIPTION	REVISED	DATE